

US 2007/0156566

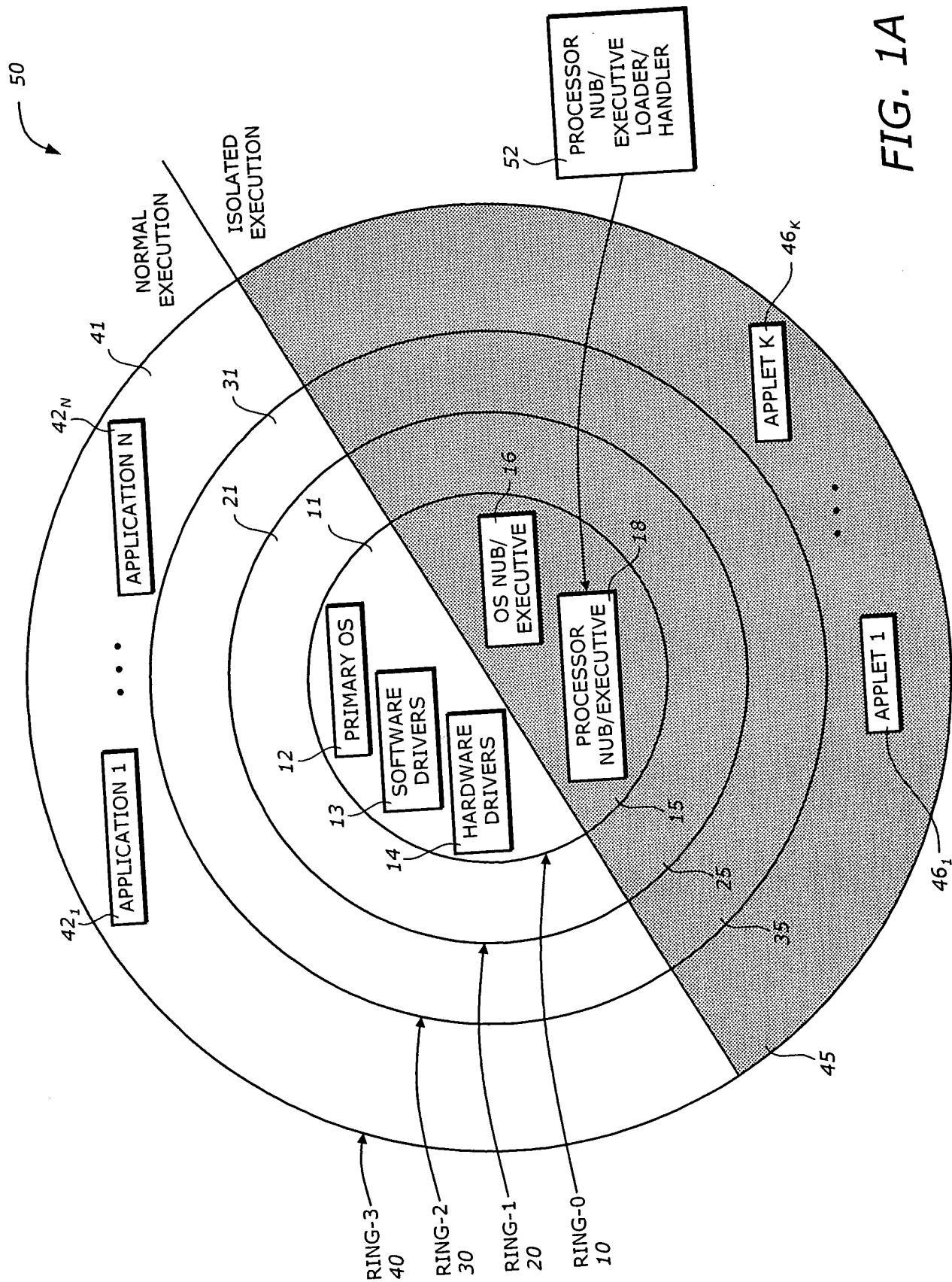


FIG. 1A

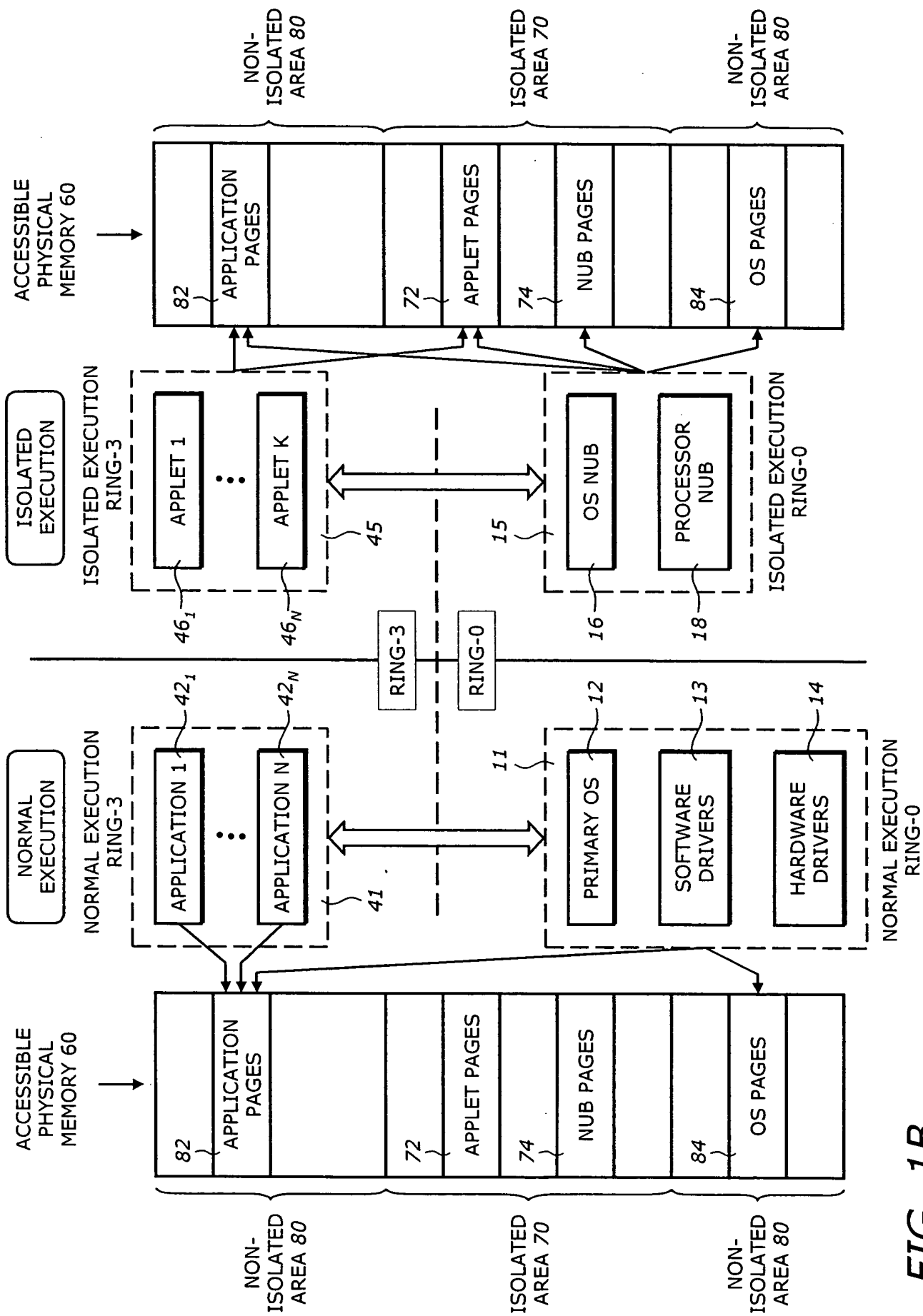


FIG. 1B

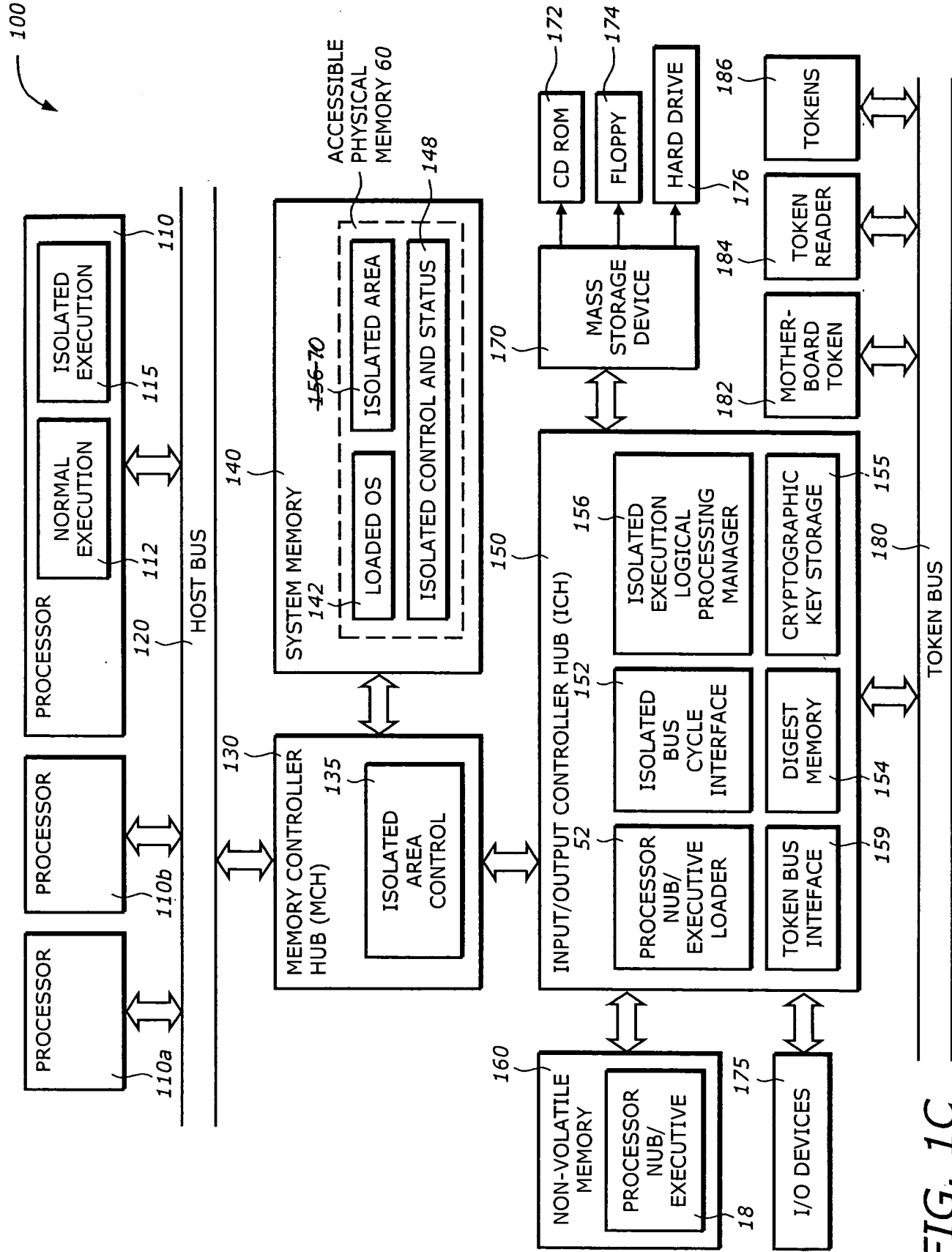


FIG. 1C

115

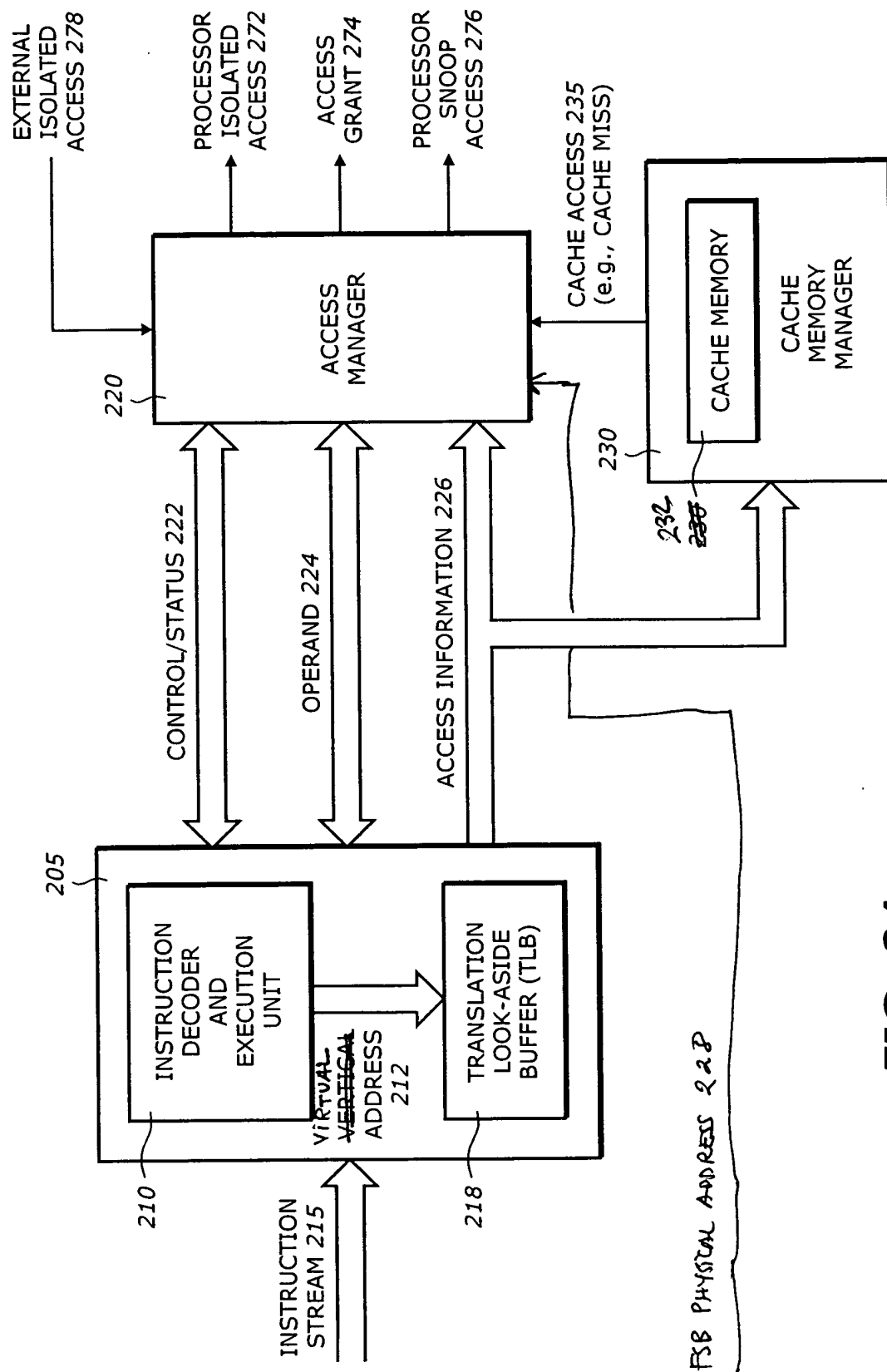


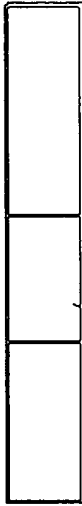
FIG. 2A

EXTERNAL ISOLATED ACCESS 278 ————— 220

CACHE ACCESS 235 —————

CONFIGURATION STORAGE 250

PROCESSOR CONTROL REGISTER 252



EXECUTION MODE WORD 253

ISOLATED SETTING REGISTER 260

ISOLATED SETTING 268

MASK REGISTER 262

ISOLATED MASK VALUE 263

BASE REGISTER 264

ISOLATED BASE VALUE 265

OPERAND  
224

LENGTH REGISTER 266

ISOLATED LENGTH VALUE 267



FSB PHYSICAL ADDRESS 228

PHYSICAL ADDRESS 282

CACHE ACCESS 235 { READ/WRITE (RD/WR#) 284

ACCESS TYPE 235

270

ACCESS  
CHECKING  
CIRCUIT

PROCESSOR  
ISOLATED  
ACCESS 272

ACCESS  
GRANT 274

PROCESSOR  
SNOOP  
ACCESS 276

FIG. 2B

270

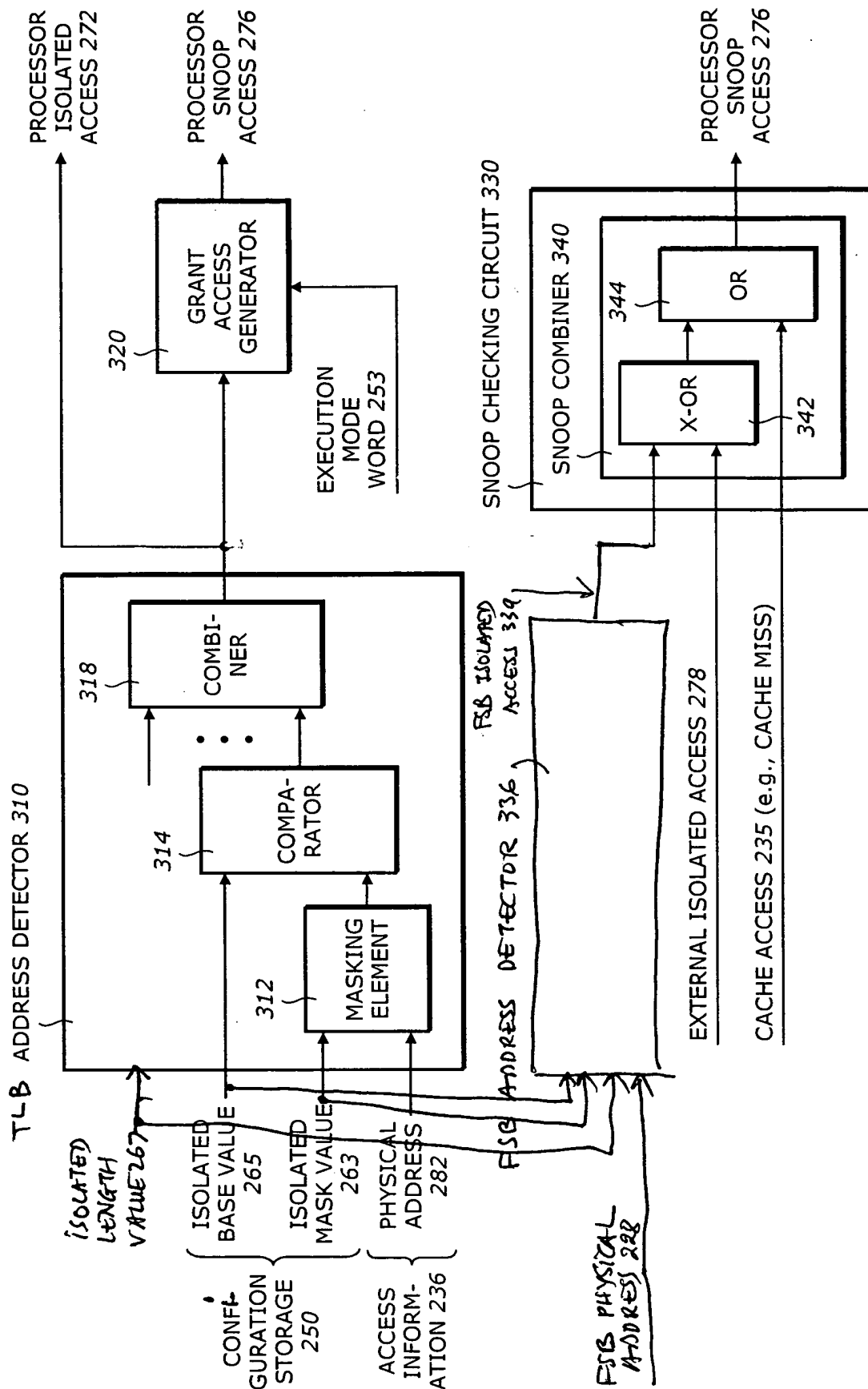


FIG. 3A

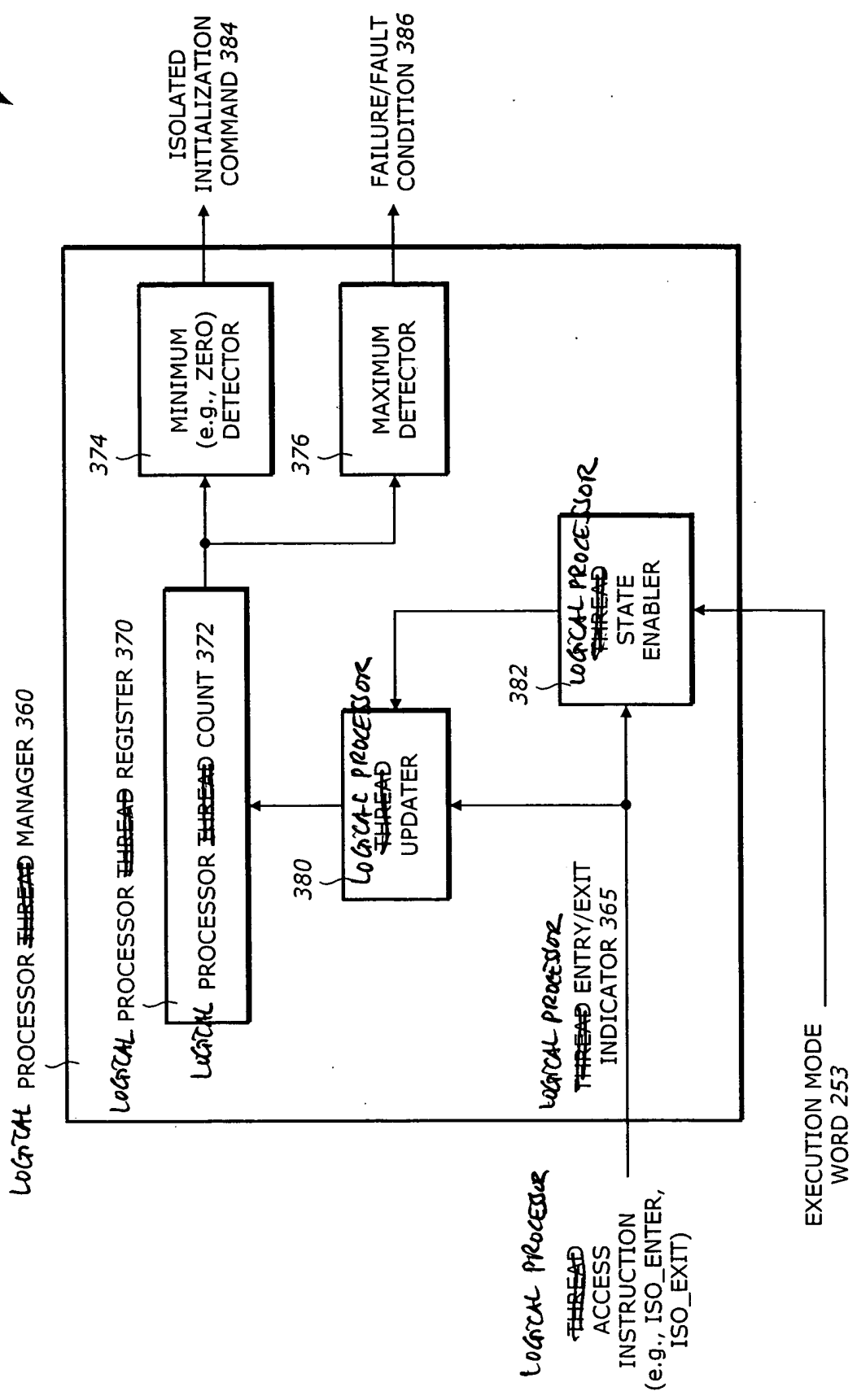


FIG. 3B

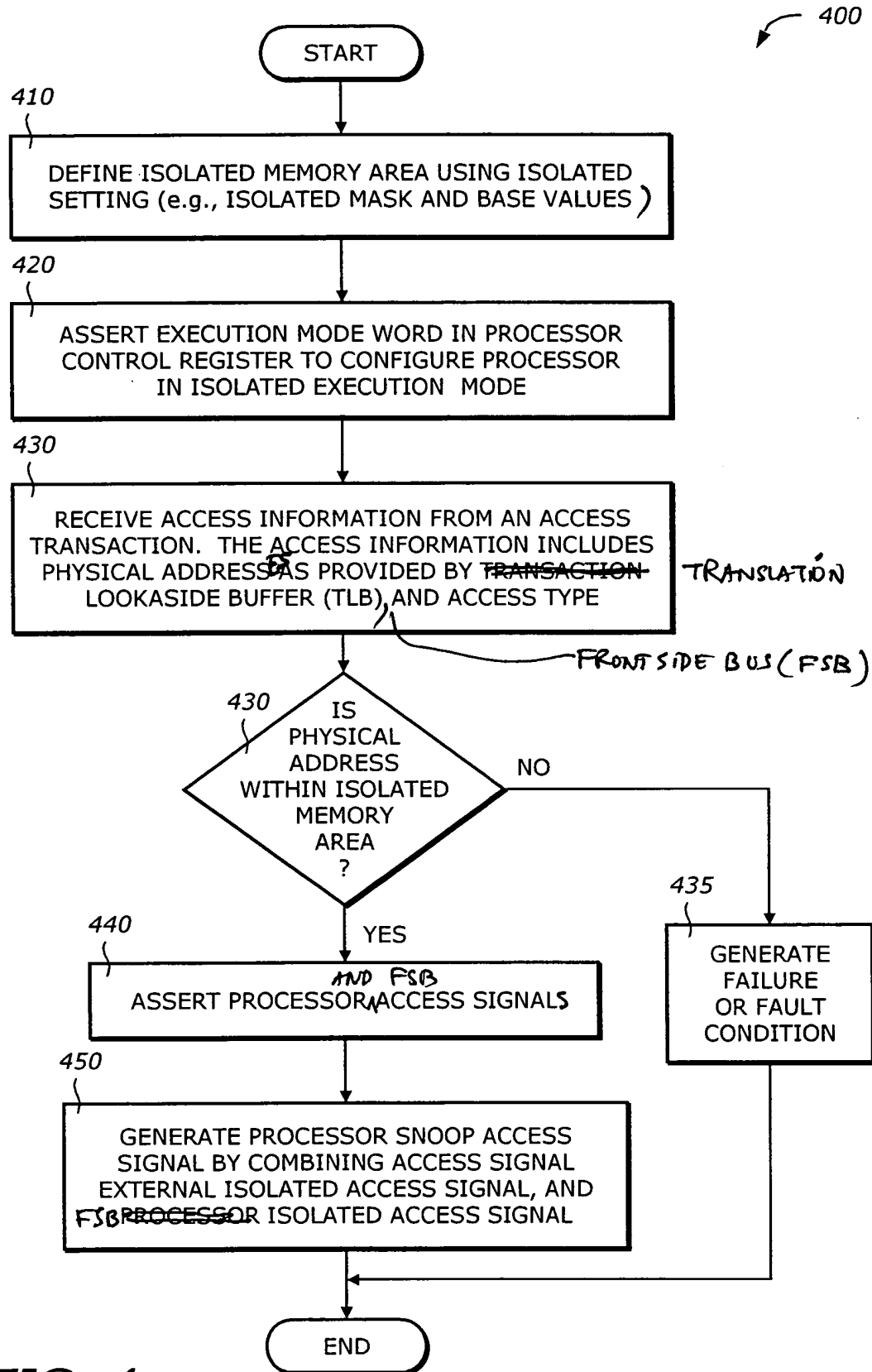


FIG. 4



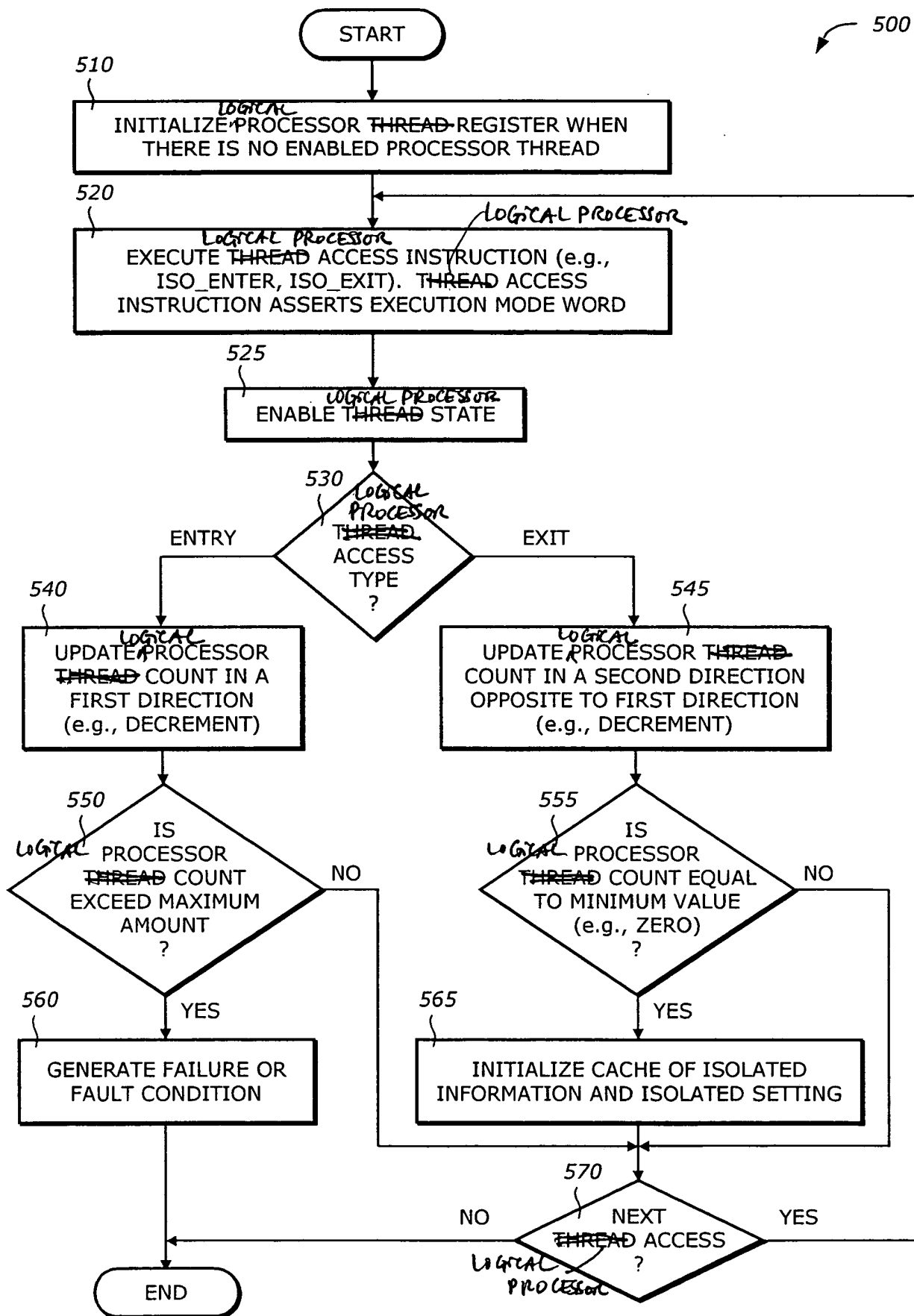


FIG. 5